

FIG. 1

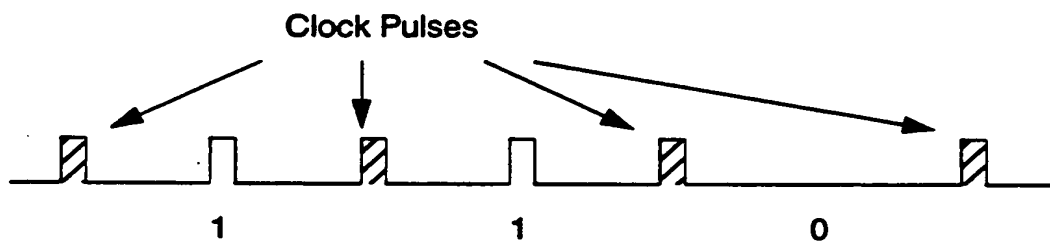
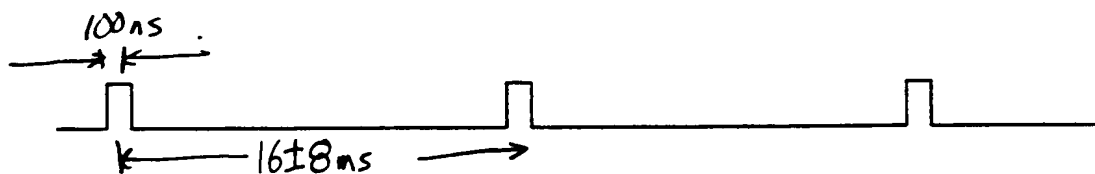


FIG. 2

25-

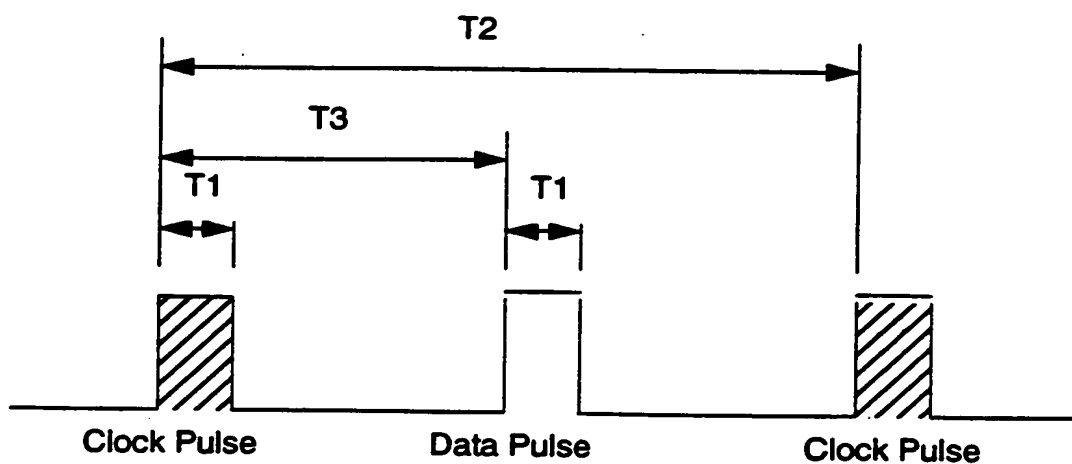
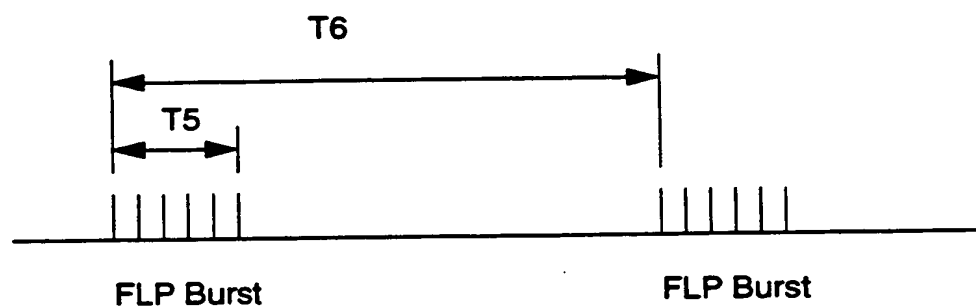


FIG. 3

FIG. 4FIG. 5

#	Parameter	Min	Typ	Max	units
T1	Clock / Data Pulse Width		100		ns
T2	Clock Pulse to Clock Pulse		125		μs
T3	Clock Pulse to Data Pulse (Data = 1)		62.5		μs
T4	Pulses in a Burst	17		33	#
T5	Burst Width		2		ms
T6	Burst to Burst	8	16	24	ms

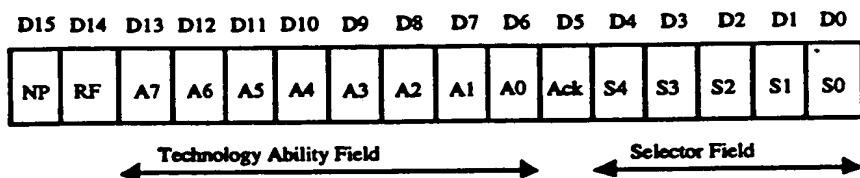


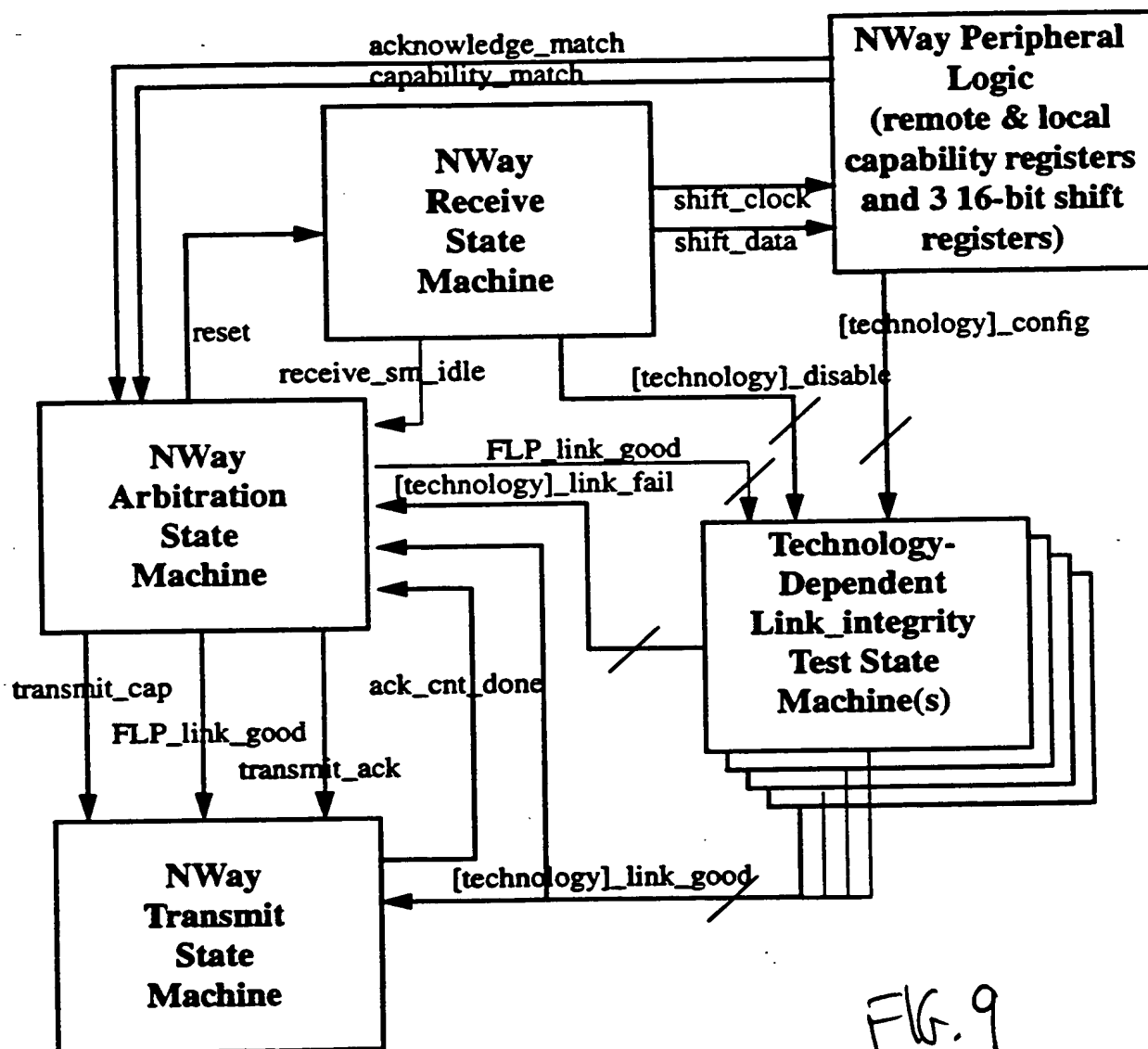
FIG. 6

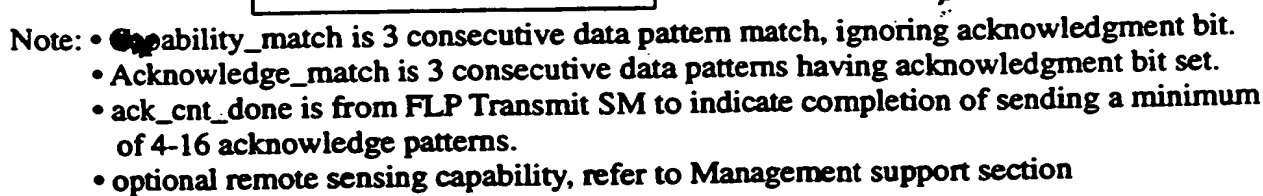
D10	D9	D8	D7	D6	Description
0	0	0	0	1	10BASE-T
0	0	0	1	0	10BASE-T Full Duplex
0	0	1	0	0	100BASE-TX
0	1	0	0	0	100BASE-TX Full Duplex
1	0	0	0	0	100BASE-T4

FIG. 7

FIG. 8

D10	D9	D8	D7	D6	Description
0	0	0	1	1	Regular and Full Duplex 10BASE-T
1	0	1	0	1	10BASE-T, 100BASE-TX, and 100BASE-T4 all supported in normal configurations.
0	0	1	0	1	10BASE-T and 100BASE-TX configurations both supported.
1	1	1	0	0	100BASE-TX, 100BASE-T4, 100BASE-TX Full Duplex supported.





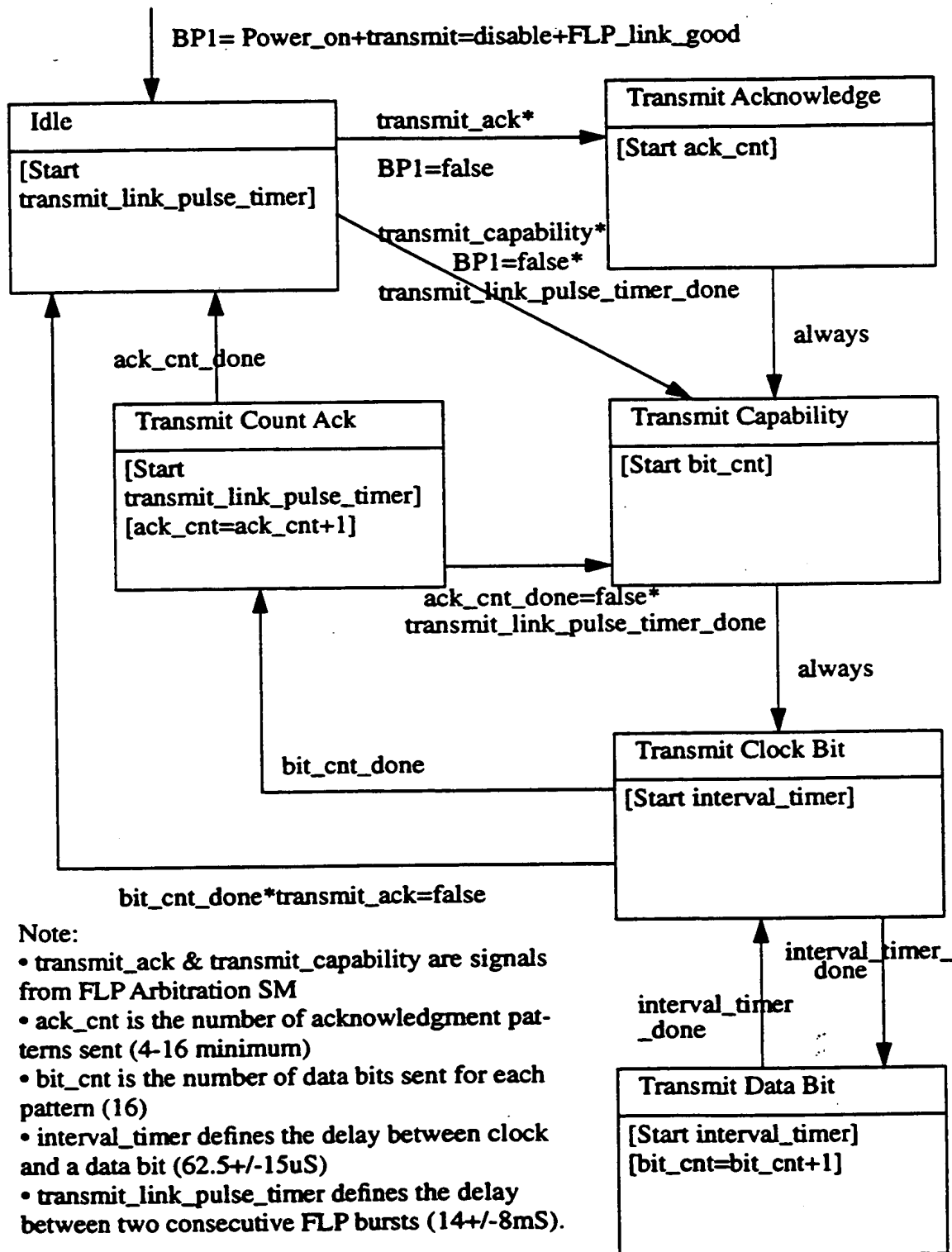


FIG. 11

FIG. 12

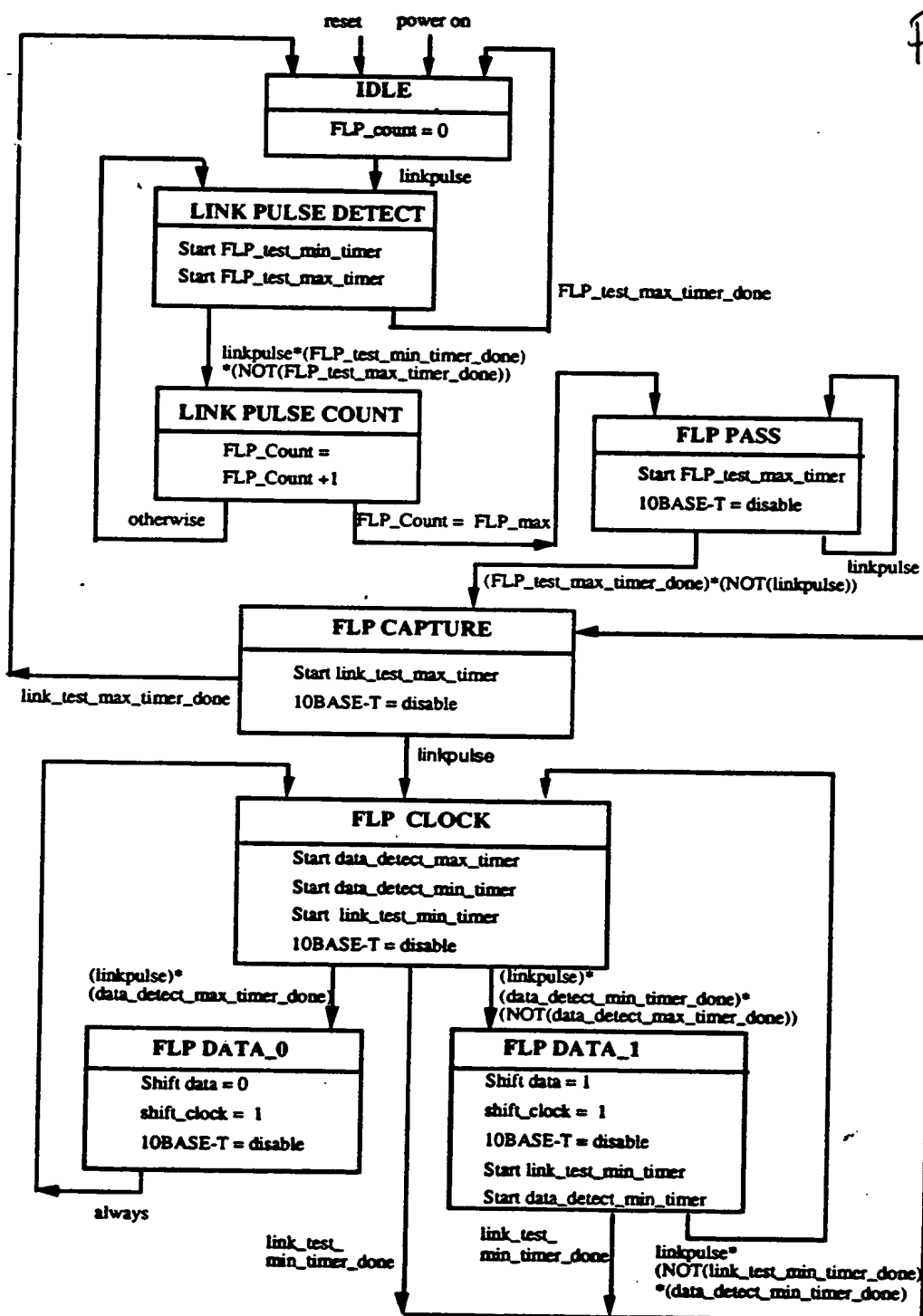


FIG. 13

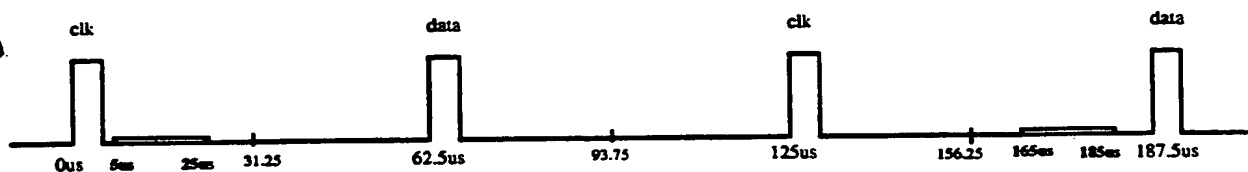


FIG. 14

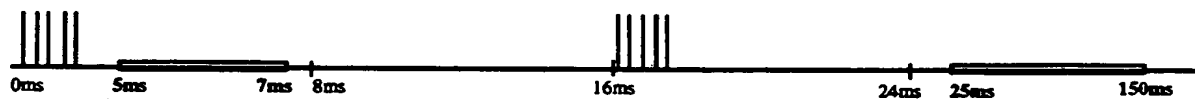
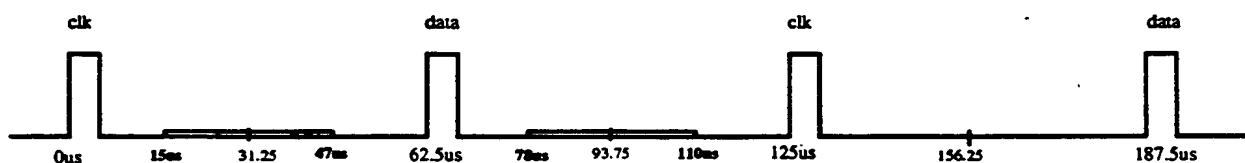


FIG. 15



Parameter	Min	Typ	Max	units
FLP_test_min_timer	5		25	μs
FLP_test_max_timer	165		185	μs
Link_test_min_timer	5		7	ms
Link_test_max_timer	50		150	ms
Data_detect_min_timer	15		47	μs
Data_detect_max_timer	78		110	μs

FIG. 16

FIG. 17

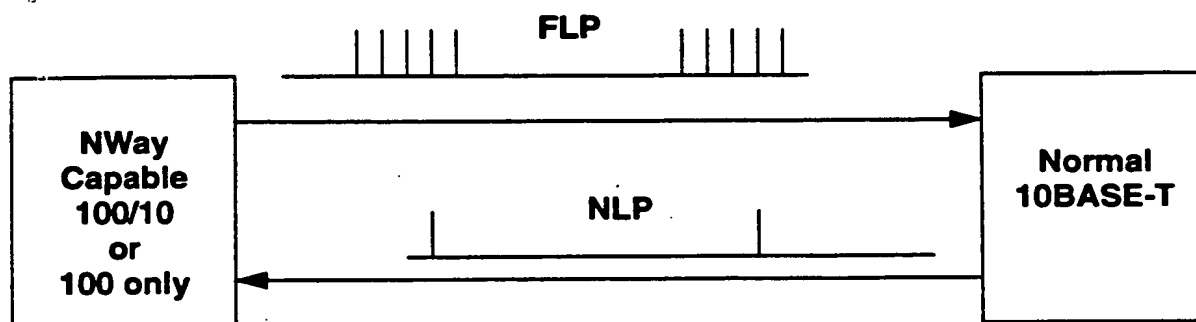


FIG. 18

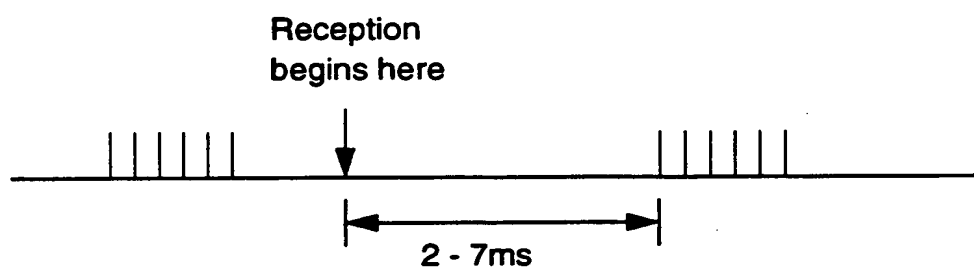


FIG. 19



FIG. 20

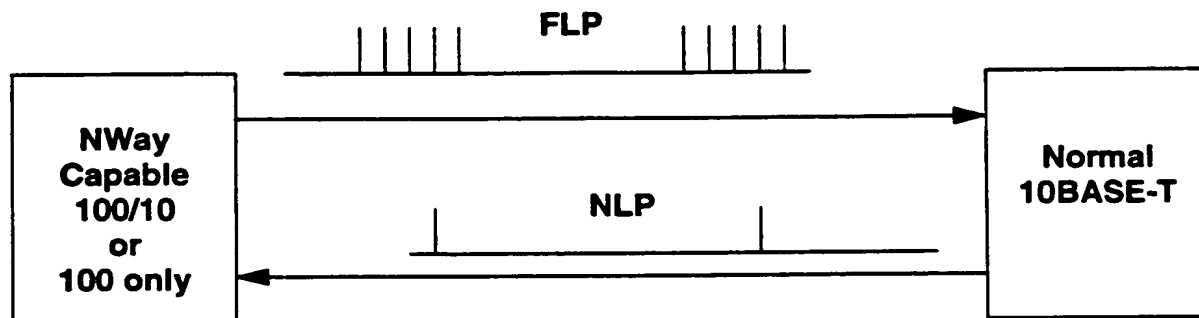


FIG. 21

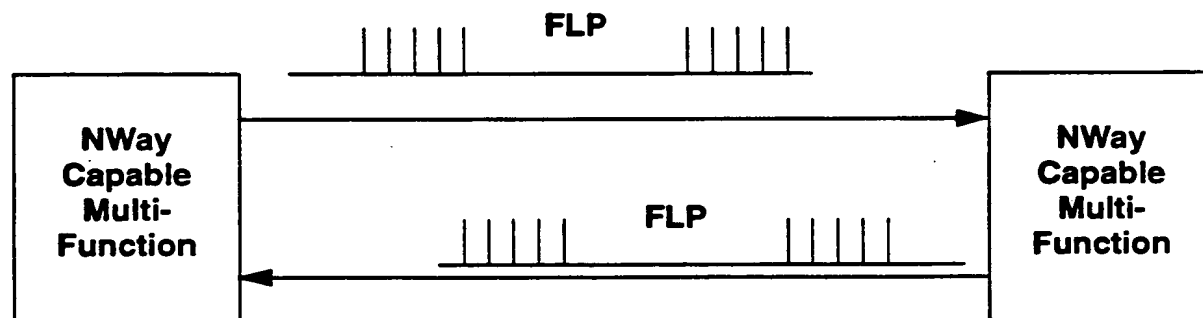


FIG. 22

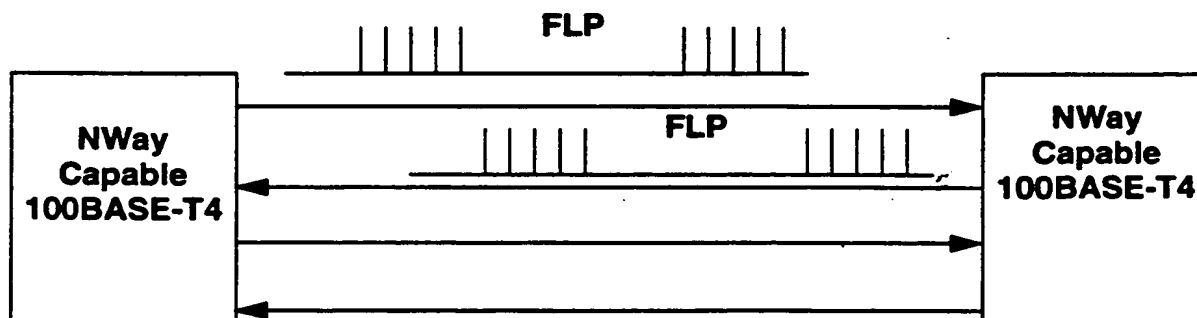
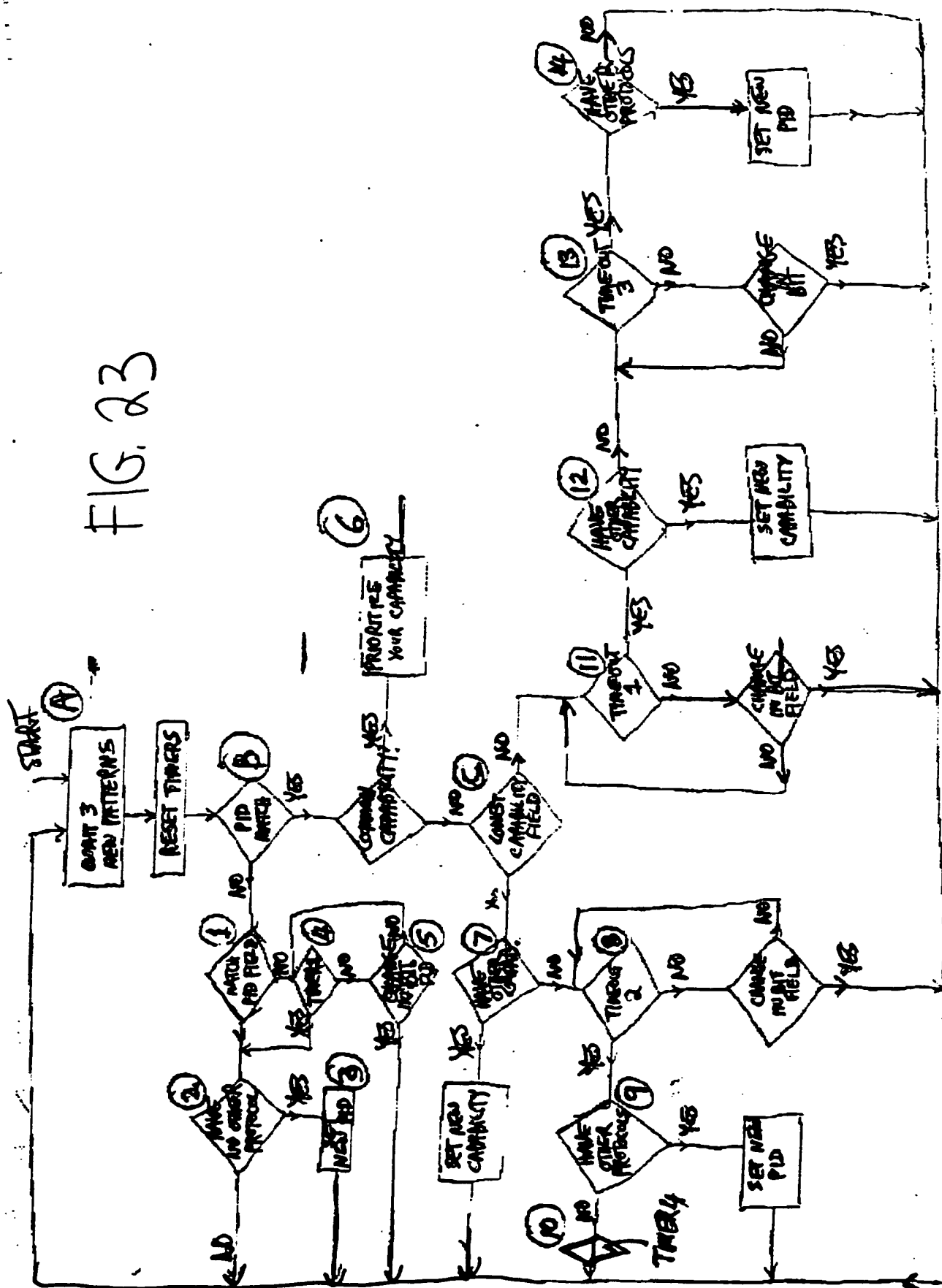


FIG. 23



Data Word Encoding (Multi-Protocol Negotiation)

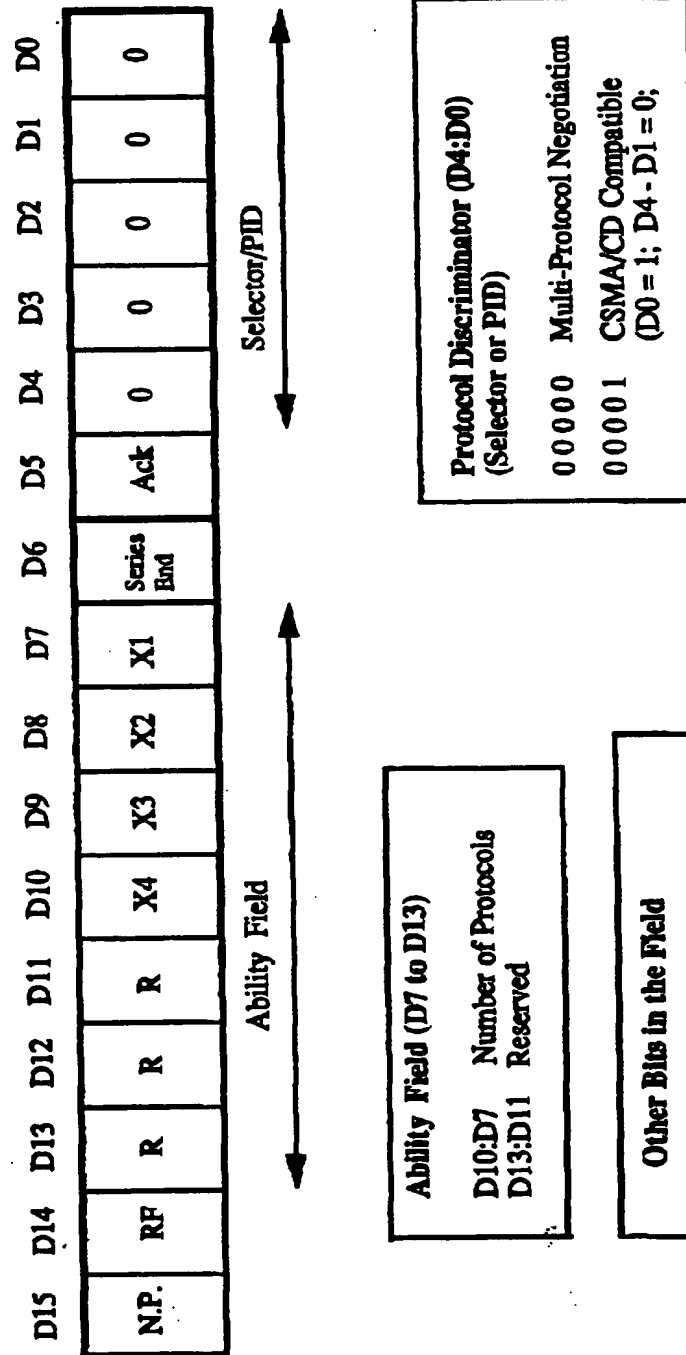


FIG. 24

Example of a small network

